

## Refine Search

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### Search Results -

<b>Terms</b>	<b>Documents</b>
(717/162).ccls.	161

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**Database:**

US Pre-Grant Publication Full-Text Database <b>US Patents Full-Text Database</b> US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins
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**Search:**

L10	<span style="font-size: 2em; vertical-align: middle;"> </span> <span style="font-size: 1.5em; vertical-align: middle;">Refine Search</span>
<span style="font-size: 1.5em; vertical-align: middle;"> </span> <span style="font-size: 1.5em; vertical-align: middle;">Recall Text</span>	<span style="font-size: 1.5em; vertical-align: middle;"> </span> <span style="font-size: 1.5em; vertical-align: middle;">Clear</span>
<span style="font-size: 1.5em; vertical-align: middle;"> </span> <span style="font-size: 1.5em; vertical-align: middle;">Interrupt</span>	

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### Search History

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**DATE:** Monday, May 17, 2004   [Printable Copy](#)   [Create Case](#)

<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side				
		DB=USPT; PLUR=YES; OP=ADJ		
L10	717/162.ccls.		161	L10
L9	717/136,137,138,139,140.ccls.		531	L9
		DB=TDBD; PLUR=YES; OP=ADJ		
L8		source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	0	L8
		DB=DWPI; PLUR=YES; OP=ADJ		
L7		source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	1	L7
		DB=JPAB; PLUR=YES; OP=ADJ		
L6		source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	0	L6

*DB=EPAB; PLUR=YES; OP=ADJ*

L5 source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)

0 L5

*DB=USOC; PLUR=YES; OP=ADJ*

L4 source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)

0 L4

*DB=PGPB; PLUR=YES; OP=ADJ*

L3 source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)

1 L3

*DB=USPT; PLUR=YES; OP=ADJ*

L2 L1 and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)

20 L2

L1 source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$)

153 L1

END OF SEARCH HISTORY